

Figure 1A (Prior Art)

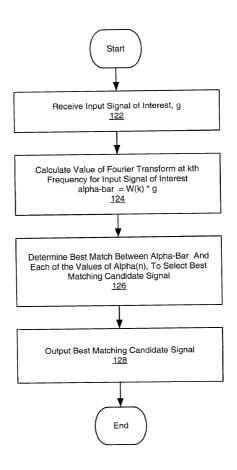
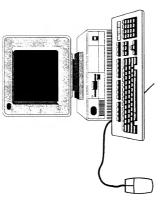


Figure 1B (Prior Art)



Host Computer System 202

Figure 2

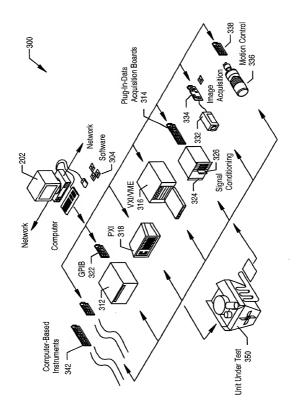


FIG. 3A

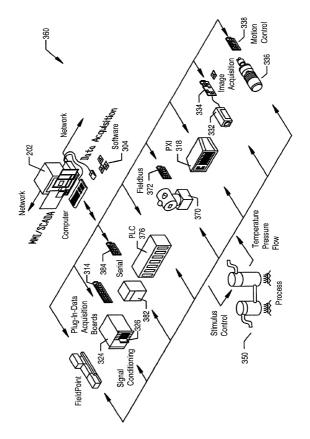


FIG. 3B

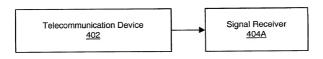


Figure 4A

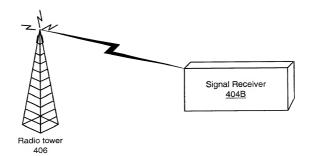


Figure 4B

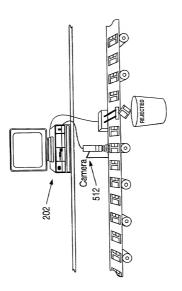


Figure 5

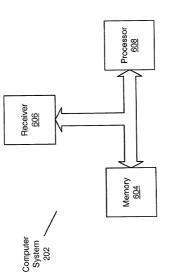


Figure 6

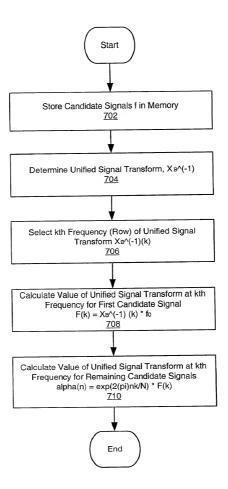


Figure 7A

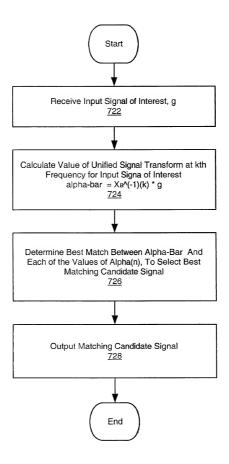


Figure 7B

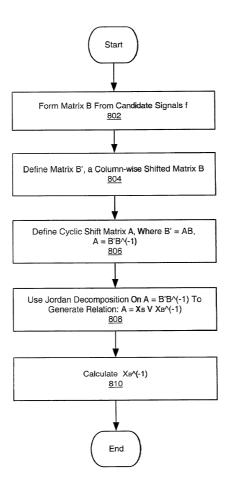
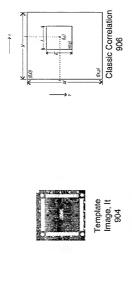


Figure 8



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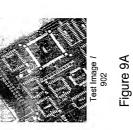


Figure 9B

Figure 9C (Prior Art)



Reference Pattern 1002



Sampled Pattern 1004

Edges Information 1006

Shift Invariant Pattern Matching

Figure 10

